| INFORMATION DISCLOSURE CITATION IN AN APPLICATION | ATTY. DOCKET NO.<br>043876-0144 | SERIAL NO.<br>10/616,303 |
|---|---|---|
| | APPLICANT<br>**John MOUSSOURIS, et al.** | |
| **(PTO-1449)** | FILING DATE<br>**July 10, 2003** | GROUP<br>**2671** |

## U.S. PATENT DOCUMENTS

| EXAMINER'S INITIALS | CITE NO. | Document Number<br>Number-Kind Code2 (if known) | | Publication Date<br>MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|---|---|---|---|---|---|---|
| | | US | | | | |
| | | US | | | | |
| | | US | | | | |
| | | US | | | | |
| | | US | | | | |
| | | US | | | | |

## FOREIGN PATENT DOCUMENTS

| EXAMINER'S INITIALS | CITE NO. | Foreign Patent Document<br>Country Code3 -Number 4 -Kind Codes (if known) | Publication Date<br>MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines Where Relevant Figures Appear | Translation | |
|---|---|---|---|---|---|---|---|
| | | | | | | Yes | No |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

| EXAMINER'S INITIALS | CITE NO. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | |
|---|---|---|---|
| /DW/ | | IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications and Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (Mar. 1992) (50006DOC018530 – 563) | |
| ↓ | | IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)," IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995) (50006DOC018413 – 529) | |
| | | Gerry Kane et al., "MIPS RISC Architecture," Prentice Hall (1995) (50006DOC018576 –848) | |
| /DW/ | | Undy et al., "A Low-Cost Graphics and Multimedia Workstation Chip Set," IEEE Micro, pp. 10-22 (April 1994) (51056DOC002578 – 590) | |

| EXAMINER<br>/Daniel Washburn/ | DATE CONSIDERED<br>03/09/2007 |
|---|---|

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.